

Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

Q.I.P.F. PATENT DATE
SCANNED Q.A. CS1

APPLICATION NO. 09/691353	CONT/PRIOR	CLASS 438	SUBCLASS 142	ART UNIT 2823	EXAMINER KING, V. J.
<p>APPLICANTS: James Adkisson Paul Angello Arne Ballantine Rama Divakaruni</p> <p>TITLE: Method of fabricating semiconductor side wall fin</p>					

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	
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